# **Record of Version**

Version N	Iodifications Date	
1.0	Generation first version	2003-8-2

# **CONTENT**

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## 1. General Specifications

Number of characters:  $40 \text{ (W)} \times 2 \text{ (H)}$ 

Module Size:  $182.0 \text{ (W)} \times 33.5 \text{ (H)} \times 13.6 \text{ (D)}$ 

Viewing Area: 155.0 (W)× 16.0 (H)

Active Area: 147.5 (W)× 11.5 (H)

**Characters Size:** 3.20 (W)×5.55 (H)

**Characters Pith:** 3.70 (W)× 5.95 (H)

LCD display type: STN Yellow Green

Drive method: 1/16 Duty 1/5 Bias

Viewing direction: 6 o'clock

Operating Temperature: 0~+50°C

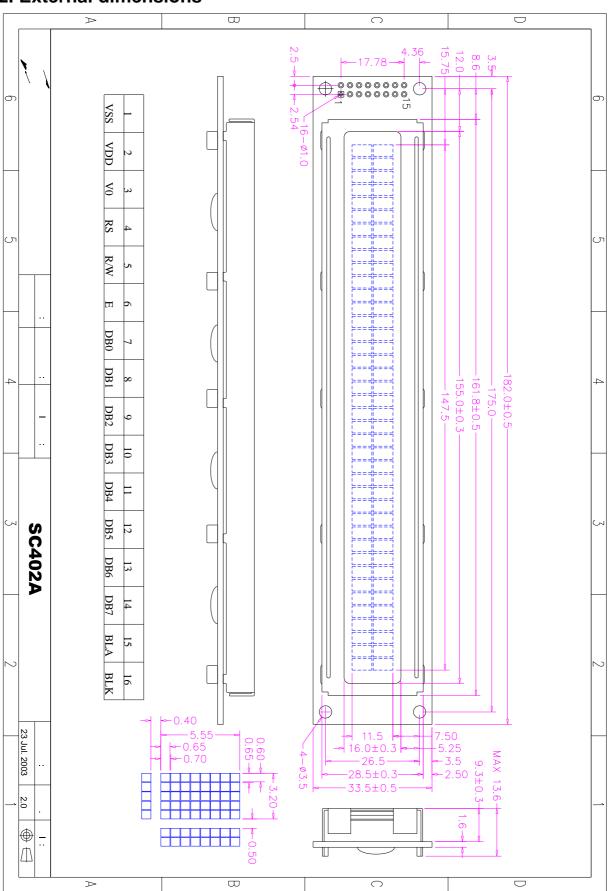
Storage temperature: -10~+60°C

Backlight: Yellow Green (LED)

Controller/driver: S6A0069

Approx. weight: 82 g

## 2. External dimensions



# 3. Maximum absolute limit

Characteristics Symbol		MIN.	MAX.	UNIT
Power Supply for Logic	VDD-Vss -0.37	7 O V		
Power Supply for LCD	VDD-V0 0 17 0	\ \		
Input Voltage	Vin	-0 3	VDD V	

**Note:** Stresses beyond those given in the Absolute Maximum Rating table may cause operational errors or damage to the device.

## 4. Electrical characteristics

## 4.1) DC characteristics (ta = $25^{\circ}$ C, V DD = 5.0 V)

,	,				
Parameter	Sym.	Min.	Тур.	Max. Unit	Condit
Operating Voltage	VDD 5.0	V			
Input high voltage	VIH 0.8	VDD	- V	DD	V
Input low voltage	VIL	Vss - 0.2		VDD	V
Output high voltage	VOH 0.8	VDD	- V	DD	V
Output low voltage	VOL	Vss	- 0.2	VDD V	
Supply current for logic	IDD - 2.0 r	nA			
Supply current for Backlight	IBLA 110	mA			

# 4.2) AC characteristics (TA = 25°C, VDD =5.0 V) Read/Write Characteristics (6800-Series Microprocessor)

Data Output Delay Time

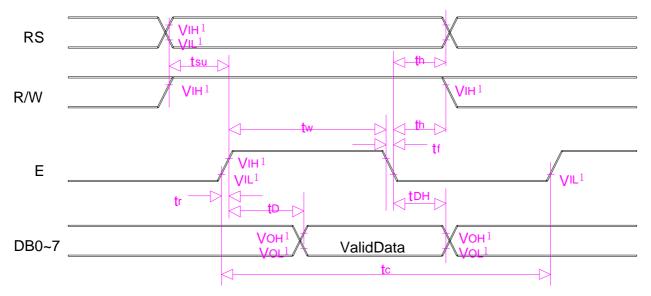
Data Hold Time

Mode	Characteristics	Symbol	Min	Тур	Max	Unit
	E Cycle Time	tc	500	-	-	ns
	E Rise/Fall Time	tR, tF	_	-	20	
	E Pulse Width (High, Low)	tw	230	-	-	
Write Mode	R/W and RS Setup Time	tsu1	40	-	-	
	R/W and RS Hold Time	tH1	10	_	-	
	Data Setup Time	tsu2	80	-	-	
	Data Hold Time	tH2	10	-	-	
	E Cycle Time	tc	500	-	-	ns
	E Rise/Fall Time	tR, tF	-	-	20	
	E Pulse Width (High, Low)	tw	230	-	-	
Read Mode	R/W and RS Setup Time	tsu	40	-	-	
	R/W and RS Hold Time	tH	10	-	-	

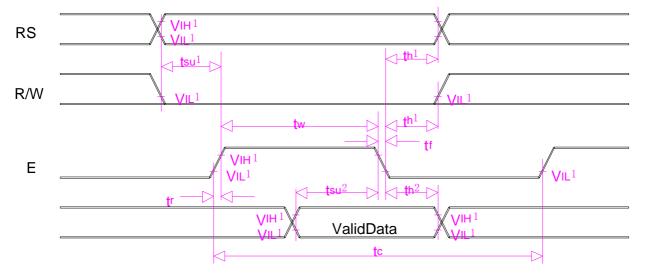
tD

tDH

120



# Read Mode Timing Diagram



 $Write {\bf Mode Timing Diagram}\\$ 

# 5. Operating Principles

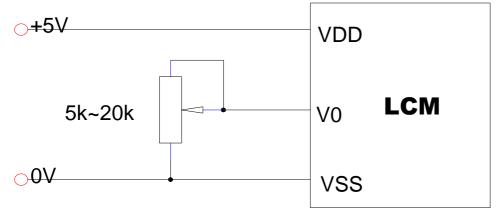
	J .	
Pin NO.	Symbol	LEVEL
1	VSS 0V GN	D
2	VDD	5V
3	V0	-
4	RS	H/L
5	R/W	H/L
6 E		H, H→L
7~10	DB0~DB3	H/L
11~14	DB4~DB7	H/L
15	BLA	+5V
16	BLK	0V

#### **Function**

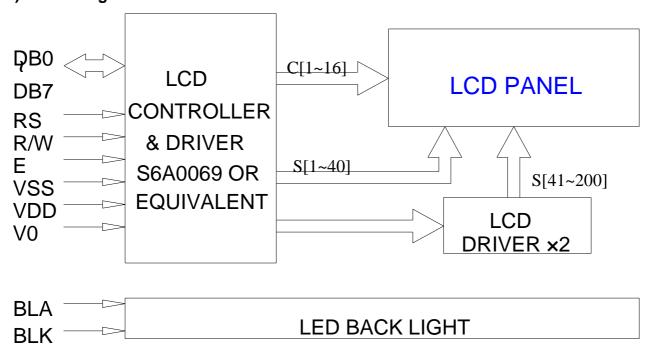
Supply Power for logic
VLCD voltage adjustment pin
Data or Instruction select.
Read or Write
Read /Write enable signal.
Data BUS for 8-bit bus mode
Data BUS for 8-bit or 4bit bus mode
Supply Power for LED light

Supply Power for LED light

## 5.2) Power supply for LCD Module



#### 5.3) Block Diagram



6. Instructi	on∕∕ī	)	crib	tion	DB5	DB4	DB3	82	81	DB0		
Instruction		R	i i	D	[Q	[Q	Q	[Q	DBC	[Q	Description	Execution time (fsoc=270K)
Clear Display	0 0	0 0	0 0	0001							Write "20H" to DDRAM. and set DDRAM address to "00H" from AC	1.53 mS
Return Home	0.0	0 0	0 0	0				0	1	Х	Set DDRAM address to "00H"from AC and return cursor to itsoriginal position if shifted.  The contents of DDRAM are not changed.	1.53 mS
Entry Mode Set	0 0	0 0	0 0	0				1	I/D	S H	Assign cursor moving direction and make shift of entire display enable.	39 uS
Display ON/OFF Control	0.0	0 0	0 0	1				D	С	В	Set display ( D ), cursor ( C ), and blinking of cursor ( B ) on/off control bit.	39 uS
Cursor or Display Shift	0 0	0 0	0 1				S / C	R / L	XX		Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	39 uS
Function Set	0.0	0 0	1 DI	N				F	X	X	DL: Set interface Data Length DL =1: 8-bit; DL =0: 4-bit; N: Numbers of display line N =1: 2-line; N =0 1-line F: Display Font type F=0: 5 × 8 dots; F=1: 5 × 11 dots	39 uS
Set CGRAM Address	0 0	0 1			A C 5	A C 4	A C 3	A C 2	A C 1	A C 0	Set CGRAM address in address counter.	39 uS
Set DDRAM Address	0 0	1		A C 6	A C 5	A C 4	A C 3	A C 2	A C 1	A C 0	Set DDRAM address in address counter.	39 uS
Read Busy Flag and Address	0.0		B F	A C 6	A C 5	A C 4	A C 3	A C 2	A C 1	A C 0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0uS
Write Data to RAM	10	D7	D6 [	)5 D	4 D3	D2D	1D0				Write data into internal RAM (DDRAM/CGRAM).	43uS
Read Data From RAM	0 1	D7	D6 [	)5 D	4 D3	D2D	1D0				Read data from internal RAM (DDRAM/CGRAM).	43uS

**NOTE:** When an MPU program with checking the Busy Flag (DB7) is made, it must be necessary 1/2 fosc is necessary for executing the next instruction by the falling edge of the 'E' signal after the Busy Flag (DB7) goes to "LOW".

Clear Display	CI	ear	Dis	olav
---------------	----	-----	-----	------

RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0	
1 00000000001	

It clears the whole display and sets display data RAM's address 0 in address counter.

#### **Return Home**

RS R/W DB7 DB6	DR5_DR4_DR3	DR2 DR1 DR0	
TO T	DDC DD I DDS	DDI DDI	
000000001			

X: Do not care (0 or 1)

It sets display data RAM's address 0 in address counter and display returns to its original position. The cursor or blink goes to the left edge of the display (to the 1st line if 2 lines are displayed).

The contents of the Display Data RAM do not change.

#### **Entry Mode Set**

During writing and reading data, it sets cursor move direction and shifts the display.

RS	R/W	DR7	DR	6 DR5	DB4	DR3	DR2	DR1	DR	<u> </u>		
110	10, 11	7		0000					DD.	,	I/D	SH

I/D = 1: Increment, I/D = 0: Decrement. S = 1: The display shift, S = 0: The display does not shift

SH= 1	I/D= 1	It shifts the display to the left
SH= 1	I/D=0	It shifts the display to the right

#### **Display On/Off Control**

RS I	P/W	DR7	DR6	DR5	DR4	DR3	DR2	DE	31 DB(	)	
140 1	<b>L</b> ) <b>V V</b>	ישם	DD	DBC	DD.	DDJ	002	DI	) DD		
0 0	000	0 1	DCB	3							

D = 1: Display on, D = 0: Display off; C = 1: Cursor on, C = 0: Cursor off

B = 1: Blinks on, B= 0: Blinks off;

#### **Cursor or Display Shift**

Without changing DD RAM's daters, it can move cursor and shift display

RS	$\mathbf{R}/\mathbf{W}$	DR7	DR6	DB5	DR4	DR3	DR2	DR1	DRO		
			220	DDC	<b>DD</b> .	<b>D</b> D3	<i>D</i> 17	-	220		1
0	000	018	S/C						R/L	ΧX	

S/C R/L	Descri	ption	Address Counter
0	0		AC = AC - 1
0	1	Shift cursor to the right	AC = AC + 1
1	0	Shift display to the left. Cursor follows the display shift	AC=AC
1	1	Shift display to the right. Cursor follows the display shift	AC=AC

#### **Function Set**

AS A V BB BB BB BB BB BB BB		DRO		DB			DR4		)R6	)R7	W	R	RS
		DDO	-		7-	PDO	<del></del>	Do	720	~	* *	-	140
		<b>v</b>	$\sim$	NIE						М	<u>.</u> ۸	$\wedge$	

X: Do not care (0 or 1)

DL: It sets interface data length.

DL = 1: Datas are transferred with 8-bit lengths (DB7 - DB0).

DL = 0: Datas are transferred with 4-bit lengths (DB7 - DB4). ( It needs two times to transfer datas )

N: It sets the number of the display line.

N = 0: One-line display.

N = 1: Two-line display.

F: It sets the character font

F = 0: 5 x 7 dots character font.

F = 1: 5 x 10 dots character font

N	F	No. of Display Lines	Character Font	Duty Factor
0	0	1	5X7 dots	1 /8
0	1	1	5x 10 dots	1 /11
1	Х	2	5x7 dots	1 /16

#### **Set Character Generator RAM Address**

DS D/W DR7	DR6 DR5 DR4	DR3 DR2 DI	R1 DR0	1	
KO KI II DDI	DDO DDO DD	DDS DD2 D	) DD		
00b1	AC5	AC4 AC3	AC2	AC1	AC0

It sets character generator RAM address AC[5:0] to the address counter. Character generator RAM data can read or write after this setting.

#### **Set Display Data RAM Address**

DC D/W	DR7 DR6 DR5	DR4 I	DR3 DI	R2 DR	1 DR0		
140 14 11	DD, DDG DDG	10011	, DO D	<del></del>	I DD0		
001	AC6	AC5	AC4	AC3	AC2	AC1	AC0

It sets display data RAM address AC[6:0] to the address counter.

Display data RAM can read or write after this setting.

In one-line display (N = 0),  $AC[6:0]=00H\sim4FH$ ;

In two-line display (N = 1),  $AC[6:0]=00H\sim27H$ , for the first line,  $AC[6:0]=40H\sim67H$ , for the second line.

## **Read Busy Flag and Address**

RS	R/X	DR7	DR6	DR5	DR4 I	DR3 DI	R2 DB	1 DR0		
	-4-		220	DDC		1000	- DD	1 220		
0	1 BF	:		AC6	AC5	AC4	AC3	AC2	AC1	AC0

When (BF = 1) indicates that the system is busy now, it will not accept any instruction until no Busy (BF = 0). At the same time, the address counter content's AC[6:0] is read out

#### Write Data to Character Generator RAM or Display Data RAM

PC P/W L	R7 DR6 DR	5 DR4	DR3 D	R2 DR	1 DR0	1	
10 10 11 1	H, DDG DD.	TDD I	<b>DD</b> D	<del></del>	1 000		
1 0 D7	D6	D5	D4	D3	D2	D1	D0

It writes data D[7:0] to character generator RAM or display data RAM.

## Read Data from Character Generator RAM or Display Data RAM

$\mathbf{R}$	C R/W	<u>DR7 I</u>	1R6 DR5	DR4	DR3 D	R2 DB			
	J 14	<b>D</b> 4, I	DDG DDC	ועם	DO D		I DD		
	1 1 D7		D6	D5	D4	D3	D2	D1	D0

It reads data D[7:0] from character generator RAM or display data RAM. To get the correct data readout is shown below:

- a. Set the address of the character generator RAM or display data RAM or shift the cursor instruction.
- b. Send the "Read "instruction.

 Display Character address code:

 Display position
 1
 2
 3
 4
 5~36
 37
 38
 39
 40

 DDRAM Address (HEX)
 1 Line
 00 01 02 03 04~23 24 25 26 27
 2
 2
 2 Line
 40 41 42 43 44~63 64 65 66 67
 40 45 65 66 67
 40 45 65 66 67
 40 45 65 66 67
 40 45 65 66 67
 40 45 65 66 67
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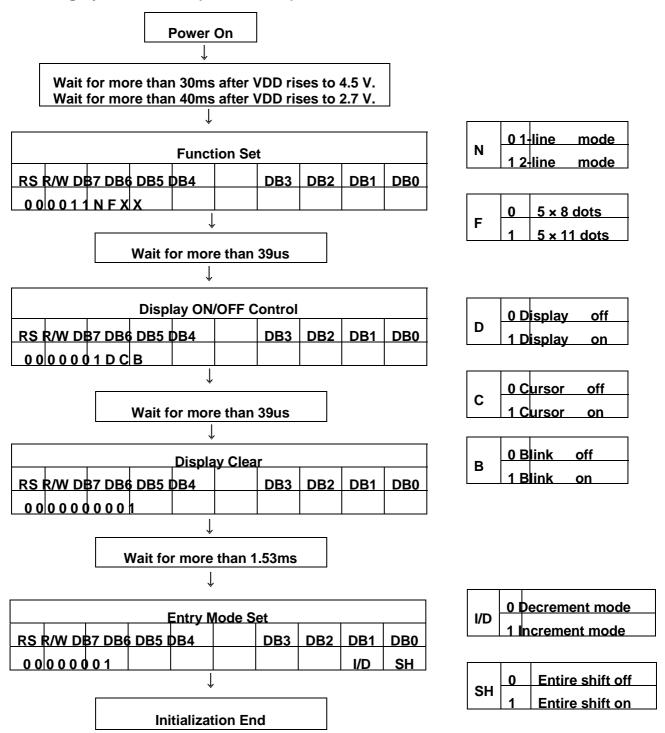
Rel	atio	nshi	p be	twe	en C	har	acte	r Co	<del>de (</del>	DDR	AM)	) and	d ch	arac	ter F	atte	<del>rn (</del>	CGR	RAM	)—		
Ch Ch	arac	ter (	ode	ന	N	M Da	0	5)	4	RAM	2	~		2	9	CK.	3 R <sub>4</sub> 4	r Pa MoÐ	ata	_	0	Pattern
			D	Δ		Δ		⋖	⋖	⋖	⋖	۷	٩		Δ							Number
								0	0	0	0	0	0	Х	Х	Х	0	1	1	1	0	
								0	0	0	0	0	1	Х	Х	Х	1	0	0	0	1	
								0	0	0	0	1	0	Х	Х	Х	1	0	0	0	1	
								_			0			Х	Х	Х	1	1	1	1		
0.0	00	X 0				0	0	0	0	0		1	1				1		-		1	Pattern 1
								0	0	0	1	0	0	Х	Х	Х	1	0	0	0	1	
									0	0	0	1	0	1	Х	Х	Х	1	0	0	0	1
							I	0	0	0	1	1	0	Х	Χ	Χ	1	0	0	0	1	
								0	0	0	1	1	1	Х	Х	Χ	0	0	0	0	0	
•••				• • • • •																		
								1	1	1	0	0	0	Х	Х	Х	0	0	0	0	0	
								1	1	1	0	0	1	Х	Х	Х	1	0	0	0	1	
								1	1	1	0	1	0	Х	Х	Х	1	0	0	0	1	
									-			_					1					
0.0	00	X 1				1	1	1	1	1	0	1	1	X	X	X	1	1	1	1	1	Pattern 8
							1	1	1	1	0	0	Х	Х	Х	1	0	0	0	1		
								1	1	1	1	0	1	Х	Х	Х	1	0	0	0	1	
						ı	1	1	1	1	1	0	Х	Х	Х	1	0	0	0	1		
								1	1	1	1	1	1	Х	Х	Х	0	0	0	0	0	

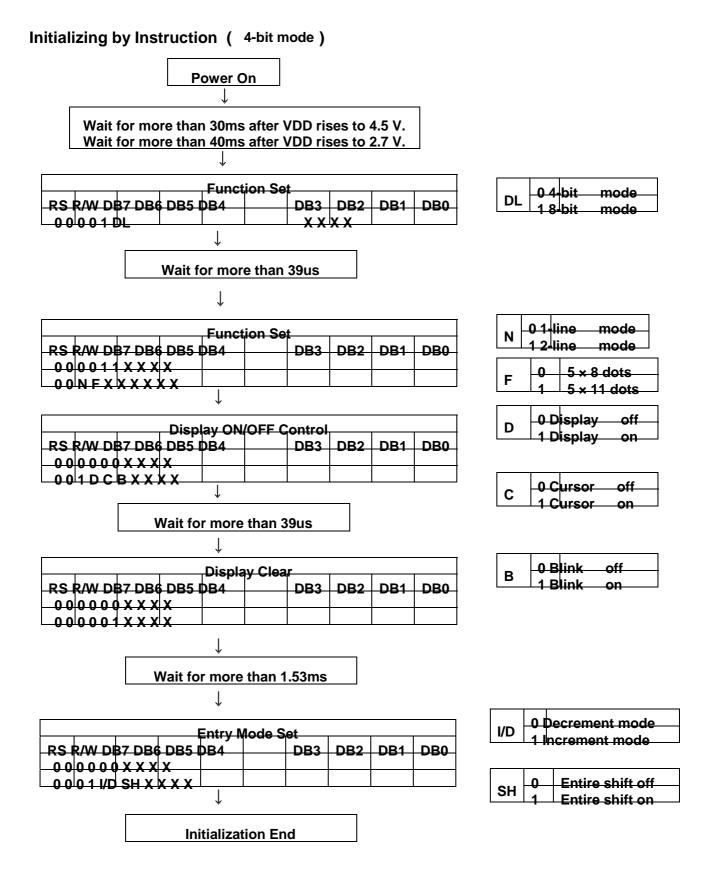
#### NOTES:

- 1. these areas are not used for display, but can be used for the general data RAM.
- 2. Character code bits D[2:0] correspond to CGRAM address A[5: 3] (3 bits: 8 types).
- 3. Character pattern row positions correspond to CGRAM data bits D[4:0] (D4 being at the left)..
- 4. " 1 ": Selected," 0 ": No selected," X": Do not care (0 or 1).
- 5. The 8th line is the cursor position and display is formed by logical OR with the cursor

CGRON	/I Cha	aracte	er Co	de Ta	ıble:	( Er	nglisł	n & Ja	apane	ese	)			Π		
Upper 4bit Lower 4bit	LLLL	LLLH	LLHL	LLHHI	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	НГНН	HHLL	HHLH	HHHL	нннн
LLLL	CG RAM (1)															
LLLH	(2)															
LLHL	(3)															
LLHH	(4)															
LHLL	(5)															
LHLH	(6)															
LHHL	(7)															
LHHH	(8)															
HLLL	(1)															
HLLH	(2)															
HLHL	(3)															
HLHH	(4)															
HHLL	(5)				NO 000 NO 000 NO 000 NO 000 NO 000 NO 000 NO 000											
HHLH	(6)															
HHHL	(7)															
нннн	(8)															

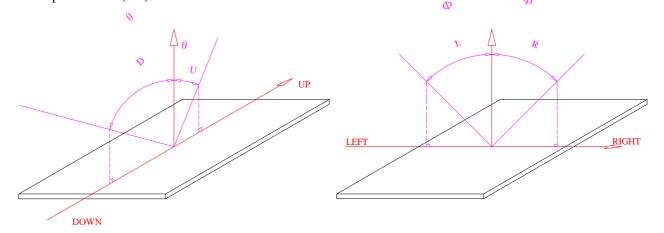
# Initializing by Instruction (8 -bit mode)



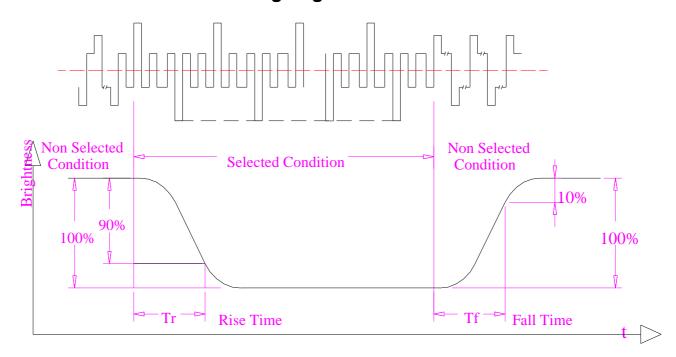


# 7° Electro-optical Units

**Electro-optical Characteristics** Standard Value **Test Condition** Unit Item Symbol Min. Tvp. Max θЦ 10 degree θД degree 40 ΦЦ CR=2Viewing Angle 30 degree ΦR 30 degree Ta=25℃ Response Time (rise) Tr 300 ms Ta=25℃ Response Time (fall) Tf 300 ms



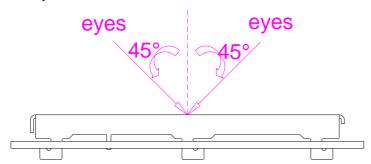
# **Viewing Angle**



CRrightness of Non-selected Segment
Brightness of Selected Segment

# 8. Inspection criteria

## 8.1.Inspection conditions

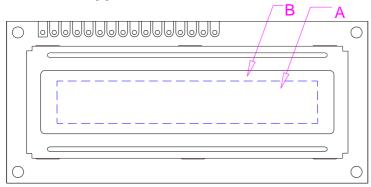


The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

The test direction is base on about around 45 degree of vertical line

## **Definition of applicable Zones**



A: Active display area

B: Area from outside of "A Zone" to validity viewing area

A + B = Validity viewing area

## b). Sampling procedures for each item's acceptance level table

Defect type	Sampling procedures	AOL
	M1L-STD-105D inspection level I	
Major defect	Normal inspection single sample inspection	1,0
Minor defeat	M1L-STD-105D Inspection level I	2.5
Minor defect	Normal inspection single sample inspection	2.5

# c) <sup>①</sup>. Major defect

A major defect refers to a defect which is not. considered to substantially degrade usability for product applications.

②. Minor defect

A minor defect refer, 10 a defect which is not considered 10 substantially degrade product application, or a defect which deviates from existing Standards almost unrelated to the effective use of the product or it's operation.

# d). Incoming Inspection standards

d). Incoming inspection standards				
ITEM	Criterion for defects	Defect type		
1.Display on inspection	(1) Non display (2) Vertical Line is deficient (3) Horizontal line is deficient (4) Cross line is deficient	Minor		
2.Black/While spot	Size $\emptyset$ (mm) Acceptable number $\emptyset^{\leq}$ 0.25 Ignore (note) $0.25 < \emptyset^{\leq}$ 0.35 3 0.35 $< \emptyset$ 0 Note: NG is four or more spot_crowd_together	Minor		
3.Black/White line	Length (mm) width (mm): Acceptable number  L<10 w<0.05 Ignore  L<10 0.05 <w<0.10 0="" 0.10="" 1="" l="" l<10="" w="" ≤="">10 w&lt;0.05 0  Defects separate at interval if 30mm each other</w<0.10>	Minor		
4.Display on inspection	Solution So	Minor		
5) Spot-like contrast irregularity	Size ø (mm) Acceptable number $\emptyset^{\leq}$ 0.2 Ignore (note) $0.2 < \emptyset^{\leq}$ 0.4 3 $0.4 < \emptyset^{\leq}$ 0.6 2 $0.6 < \emptyset$ 0 NOTE: 1) conformed to limit samples 2) Defects separate at intervals of 50mm each other	Minor		
6) Bubble in polarizer	Size $\varnothing$ (mm) Acceptable number $\varnothing^{\leq}$ 0.2 Ignore (note) $0.2 < \varnothing^{\leq}$ 0.4 3 $0.4 < \varnothing^{\leq}$ 0.6 2	Minor		
Scratches and dent on the polarizer	Scratches and dent on the polarizer shall be in the accordance with "2) Black/While spot 3) Black/White line	Minor		
8.) Stains on LCD panel surface	Stains which cannot be removed even when wiped lightly with a soft cloth or similar cleaning too.	Minor		
9) Rainbow color	No rainbow color is allowed in the optimum contrast on Stale within the active area	Minor		
10) Threshold	Non-uniform brightness at optimum contrast is not allowed and the criterion abides by standard samples	Minor		
11) Viewing area	Polarizer edge or line is visible in the opening viewing area due to polarizer shortness or sealing line.	Minor		
12) Bezel appearance	Rust and deep damage which are visible in the bezel is reject able	Minor		
13) Defect of land surface contact (Poor soldering)	Evident crevices which is visible are reject able	Minor		
14) Parts mounting	<ul><li>(1) Failure to mount parts</li><li>(2) Parts not in the specifications are mounted</li><li>(3) Polarity, for example, is reversed</li></ul>	Major		

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ITEM	Criterion for defects	Defect type
15) Parts alignment	<ul><li>(1) LSI. IC lead width is more then 50% beyond pad outline.</li><li>(2) Chip component is off center and more then 50% of the leads s off the pad outline</li></ul>	Minor Minor
16) Conductive foreign matter	(1) $0.45 < \emptyset$ , $N \ge 1$ (2) $0.30 < \emptyset \le 0.45$ , $N \ge 1$	Major Minor
	Ø: Average diameter of solder ball (unit: mm) (3) 0.50 <l (unit:="" ,="" average="" chip="" l:="" length="" mm)<="" n="" of="" solder="" td=""><td>Minor</td></l>	Minor
17) PWB pattern damage	(1) Deep damage is found on copper foil and the pattern is nearly broken	Major
18) Faulty PWB correction	<ul> <li>(2) Damage on copper foil other than (1) above</li> <li>(1) Due to PWB copper foil pattern burnout, the pattern is connected, using a jumper wire for repair; 2 or more places are corrected per PWB.</li> </ul>	Minor Minor
16) Faulty FWB Correction	(2) Short circuited part is cut, and no resist coating has been performed.	Minor
19) Bezel claw	Bezel claw missing or not bent	Minor
20) Indication of name plate (sampling indication label	<ul> <li>(1) Failure to stamp or label error, or not legible, (all acceptable if legible)</li> <li>(2) The separation is more than 1/3 for indication discoloration, In which the characters can be checked</li> </ul>	Minor

# 9. Precautions for using LCD modules

#### a) Handling of LCM

- (1) The LCD screen is made of glass. Don't give excessive external shock, or drop from a high place.
- (2) If the LCD screen is damaged and the liquid crystal leaks out, do not lick and swallow. When the liquid is attach to your hand, skin, cloth etc, wash it off by using soap and water thoroughly and immediately.
- (3) Don't apply excessive force on the surface of the LCM.
- (4) If the surface is contaminated ,clean it with soft cloth. If the LCM is severely contaminated , use Isopropyl alcohol/Ethyl alcohol to clean. Other solvents may damage the polarizer . The following solvents is especially prohibited: water , ketone Aromatic solvents etc.
- (5) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (6) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (7) Don't disassemble the LCM.
- (8) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling the LCD modules.
  - Tools required for assembling, such as soldering irons, must be properly grounded.
  - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
  - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- (9) Do not alter, modify or change the the shape of the tab on the metal frame.
- (10) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- (11) Do not damage or modify the pattern writing on the printed circuit board.
- (12) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector
- (13) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- (14) Do not drop, bend or twist LCM.

#### b) Storage

- (1) Store in an ambient temperature of 5 to 45 °C, and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
- (2) Storage in a clean environment, free from dust, active gas, and solvent.
- (3) Store in antistatic container.